

## List of scientific publications:

Dr. Wojciech Jalmuzna

Dr. Tomasz Jezynski

Title	Authors	Publication Title	Publication Year
ATCA Carrier Board with IPMI supervisory circuit	Zawada, A.; Makowski, D.; <u>Jezynski, T.</u> ; Simrock, S.; Napieralski, A.	Mixed Design of Integrated Circuits and Systems, 2008. MIXDES 2008. 15th International Conference on	2008
Control and measurement system for high quality superconducting cavities	<u>Jalmuzna, W.</u> ; Cichalewski, W.; Wenskat, M.; Karstensen, S.; Elsen, E.; <u>Jezynski, T.</u>	Real Time Conference (RT), 2010 17th IEEE-NPSS	2010
Design of eight-channel ADC card for GHz signal conversion	Habib, S.B.; Czuba, K.; <u>Jalmuzna, W.</u> ; <u>Jezynski, T.</u>	Mixed Design of Integrated Circuits and Systems (MIXDES), 2010 Proceedings of the 17th International Conference	2010
Distributed versus Centralized ATCA Computing Power	Simrock, S.; Grecki, M.; <u>Jalmuzna, W.</u> ; <u>Jezynski, T.</u> ; Koprek, W.; Pucyk, P.	Real-Time Conference, 2007 15th IEEE-NPSS	2007
Evaluation of an ATCA based LLRF system at FLASH	Simrock, S.; Butkowski, L.; Grecki, M.; <u>Jezynski, T.</u> ; Koprek, W.; Jablonski, G.; Jalmuzna, W.; Makowski, D.; Piotrowski, A.; Czuba, K.	Mixed Design of Integrated Circuits & Systems, 2009. MIXDES '09. MIXDES-16th International Conference	2009
Femtosecond precision via RF Backplane in MTCA crates	Czuba, K.; Lysiuk, A.; Barmuta, P.; Lesniak, T.; Ludwig, F.; Schlarb, H.; <u>Jezynski, T.</u> ; Barmuta, P.	Mixed Design of Integrated Circuits and Systems (MIXDES), 2013 Proceedings of the 20th International Conference	2013
Firmware upgrade in xTCA systems	Makowski, D.; Jablonski, G.; Perek, P.; Mielczarek, A.; Predki, P.; <u>Jezynski, T.</u> ; Schlarb, H.; Napieralski, A.	Real Time Conference (RT), 2012 18th IEEE-NPSS	2012
FPGA - based control system for piezoelectric stacks used for sc cavity's fast tuner	Sekalski, P.; Przygoda, K.; Napieralski, A.; <u>Jalmuzna, W.</u> ; Simrock, S.; Lilje, L.; Paparella, R.	Particle Accelerator Conference, 2007. PAC. IEEE	2007
High power amplifiers chain nonlinearity influence on the accelerating beam stability in Free Electron Laser (FLASH)	Cichalewski, W.; <u>Jalmuzna, W.</u>	Mixed Design of Integrated Circuits & Systems, 2009. MIXDES '09. MIXDES-16th International Conference	2009

Implementation of adaptive feed-forward algorithm on embedded PowerPC405 processor for FLASH accelerator	Szewinski, Jaroslaw; <b>Jalmuzna, W.</b> ; Koprek, W.; Pozniak, K.; Romaniuk, R.	EUROCON, 2007. The International Conference on "Computer as a Tool"	2007
Interfaces and Communication Protocols in ATCA-Based LLRF Control Systems	Makowski, D.; Koprek, W.; <b>Jezyński, T.</b> ; Piotrowski, A.; Jablonski, G.; Jalmuzna, W.; Simrock, S.	Nuclear Science, IEEE Transactions on	2009
Interfaces and communication protocols in ATCA-based LLRF control systems	Makowski, D.; Koprek, W.; <b>Jezyński, Tomasz</b> ; Piotrowski, A.; Jablonski, G.; <b>Jalmuzna, Wojciech</b> ; Simrock, S.	Nuclear Science Symposium Conference Record, 2008. NSS '08. IEEE	2008
Low level radio frequency control system for the european XFEL	<b>Jezyński, T.</b> ; Simrock, S.	Mixed Design of Integrated Circuits and System, 2006. MIXDES 2006. Proceedings of the International Conference	2006
MTCA.4 LLRF system for the European XFEL	Branlard, J.; Ayvazyan, G.; Ayvazyan, V.; Grecki, M.; Hoffmann, M.; <b>Jezyński, T.</b> ; Ludwig, F.; Mavric, U.; Pfeiffer, S.; Schlarb, H.; Schmidt, C.; Weddig, H.; Bin Yang; Barmuta, P.; Habib, S.B.; Butkowski, L.; Czuba, K.; Grzegorzolka, M.; Janas, E.; Piekarski, J.; Rutkowski, I.; Sikora, D.; Zembala, L.; Zukocinski, M.; Cichalewski, W.; <b>Jalmuzna, W.</b> ; Makowski, D.; Mielczarek, A.; Napieralski, A.; Perek, P.; Piotrowski, A.; Pozniak, T.; Przygoda, K.; Boltruczyk, G.; Korolczuk, S.; Kudla, M.; Szewinski, J.; Oliwa, K.; Wierba, W.	Mixed Design of Integrated Circuits and Systems (MIXDES), 2013 Proceedings of the 20th International Conference	2013
Multipurpose RF field Vector Controller for linear accelerators	<b>Jalmuzna, W.</b> ; Cichalewski, W.; Szewinski, J.	Mixed Design of Integrated Circuits & Systems, 2009. MIXDES '09. MIXDES-16th International Conference	2009
Parameter estimation and tuning of a multivariable RF controller with FPGA technique for the Free Electron Laser FLASH	Schmidt, C.; Lichtenberg, G.; Koprek, W.; <b>Jalmuzna, W.</b> ; Werner, H.; Simrock, S.	American Control Conference, 2008	2008
Prototype AdvancedTCA Carrier Board with three AMC bays	Zawada, Adam; Makowski, D.; <b>Jezyński, Tomasz</b> ; Simrock, S.; Napieralski, Andrzej	Nuclear Science Symposium Conference Record, 2008. NSS '08. IEEE	2008
Prototype real-time ATCA-based LLRF control system	Makowski, D.; Koprek, W.; <b>Jezyński, T.</b> ; Piotrowski, A.; Jablonski, G.; <b>Jalmuzna, W.</b> ; Czuba, K.; Predki, P.; Simrock, S.	Real Time Conference (RT), 2010 17th IEEE-NPSS	2010
Prototype Real-Time ATCA-Based LLRF Control System	Makowski, D.; Koprek, W.; <b>Jezyński, T.</b> ; Piotrowski, A.; Jablonski, G.; <b>Jalmuzna, W.</b> ; Czuba, K.; Predki, P.; Simrock, S.;	Nuclear Science, IEEE Transactions on	2011

	Napieralski, Andrzej		
Prototype real-time ATCA-based LLRF control system	Makowski, D.; Koprek, W.; Jezynski, T.; Piotrowski, A.; Jablonski, G.; <b>Jalmuzna, W.</b> ; Czuba, K.; Predki, P.; Simrock, S.	Real Time Conference (RT), 2010 17th IEEE-NPSS	2010
Real time cavity simulator for European XFEL	Pucyk, P.; <b>Jalmuzna, W.</b> ; Simrock, S.	Real-Time Conference, 2007 15th IEEE-NPSS	2007
RF backplane for MTCA.4 based LLRF control system	Czuba, K.; <b>Jezynski, T.</b> ; Hoffmann, M.; Ludwig, F.; Schlarb, H.	Real Time Conference (RT), 2012 18th IEEE-NPSS	2012
RF Backplane for MTCA.4-Based LLRF Control System	Czuba, K.; <b>Jezynski, T.</b> ; Hoffmann, M.; Ludwig, F.; Schlarb, H.	Nuclear Science, IEEE Transactions on	2013
Status of the laser master oscillator system at FLASH	Winter, A.; Schmuser, P.; Loehl, F.; Schlarb, H.; <b>Jalmuzna, W.</b>	Particle Accelerator Conference, 2007. PAC. IEEE	2007
uTCA-based controller	Mielczarek, A.; Makowski, D.; Jablonski, G.; Napieralski, Andrzej; Perek, P.; Predki, P.; <b>Jezynski, T.</b> ; Ludwig, F.; Schlarb, H.	Mixed Design of Integrated Circuits and Systems (MIXDES), 2011 Proceedings of the 18th International Conference	2011